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55931 Rev. 1.20 February 2018 AMD Ryzen™ Master Overclocking User's Guide Revision History 7 Revision History Date Revision Description February 2018 1.20 Aligned With The Availability Of Socket AM4 Ryzen APUs And Ryzen Apr 21th, 2024

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6442-S AMS ERIC GORR 691 SSB 6442 - S AMD TO S AMD (S5278.2) 298 By Senator Ericksen On Page 2, Beginning On Line 30 Of The Amendment, Strike All Of Sections 3 Through 19, And Mar 3th, 2024

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Controller, PCIe Bus, Etc. Is Based Off The Infinity Fabric Speed (FCLK). All Of This Occurs In An Interconnect AMD Calls "Infinity Fabric." The Infinity Fabric Clock Speed (FCLK) Is Configurable And Directly Relates To The Memory Clock (MCLK). For Ryzen 3000 CPUs, Most Will Run A 1:1 Ratio Between FCLK And MCLK, Mar 18th, 2024

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