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And Output Common Mode Of 650mV Is Used For All Differential Lines, And A Supply Voltage Of 900mV. All Differential Lines Keep The Transistors In Saturation

With Input Common Mode Of 900mV Down To 400mV.
4. Equalization Sub-Blocks A. Binary Weighted Current DAC Figure 3 The Tx FIR, Rx D Mar 1th, 2024

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MIL-PRF-55342 Para 4.8.9 95% RH, 25°C - 65°C ≤ 5.0% Resistance Shift Resistance To Soldering Heat MIL-STD-202 Method 210 And MIL-PRF-55342 Para 4.8.8.1 ≤ 5.0% Resistance Shift Resistance To Bonding Exposure MIL-PRF-55342 Para 4.8.8.2 ≤ 5.0% Resistance Shift Solderability MIL Jan 10th, 2024

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