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## **Chapter 4 Low-Power VLSI DesignPower VLSI Design**

Overview Of Power Consumption • The Average Power Consumption Can Be Expressed As  $1 \text{ Avg } C \text{ Load } V_{DD} C \text{ Load } V_{DD} F_{CLK} T_P 2$  • The Node Transition Rate Can Be Slower Than The Clock Rate. To Better Represent This Behav Mar 2th, 2024

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$T V_{Gd} = V_{Gs}$  P-type Body S D G P-type Body S D G N N N+ N+  $V_{Gs} > V_T$   $V_{Gs} > V_{Gd} > V_T$   $V_{Ds} = 0$  V T, Then 0