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Cryptographic Hardware And Embedded Systems

Rei Ueno¹, Naofumi Homma¹, Yukihiro Sugawara¹, Yasuyuki Nogami², And Takafumi Aoki¹ Highly Efficient GF(28) Inversion Circuit Based On Redundant GF Arithmetic And Its Application To AES Design Saint-Malo, September 13th, 2015 Cry 11th, 2024

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Operating Systems, Embedded Systems And Real-time Systems

Operating Systems, Embedded Systems, And Real-Time Systems Janez Puhon Ljubljana, 2015. CIP-CatalogingInPublication

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004.451(078.5)(0.034.2) PUHAN,Janez,1969-Operating Systems, Embedded Systems, And Real-Time Systems [Electronic 21th, 2024

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