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#### Ireland FAB ...

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### **Chapter 8 Memory Hierarchy And Cache Memory**

• Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory • T Cache = 1 Cycle, T MM = 100 Cycles • What Is The AMAT Of The Program From Example 1? AMAT = T Cache + MR Cache (t MM) = [1 + 0.375(100)] Cycles = 38.5 Cycles Memory Performance Example 2 Jun 2th, 2024

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# The Memory/Storage Hierarchy And Virtual Memory

Storage Device Speed Vs. Size Facts: •CPU Needs Subnanosecond Access To Data To Run Instructions At Full Speed •Faststorage (sub-nanosecond) Is Small (100-1000 Bytes) •Big Storage (gigabytes) Is Slow (15 Nanoseconds) •Hugestorage (terabytes) Is Glaciallyslow (milliseconds) Goal: •Need Many Gigabytes Of Memory May 1th, 2024

# Cache Memory And Performance Memory Hierarchy 1

Memory Hierarchy 19 CS@VT Computer Organization II ©2005-2015 CS:APP & McQuain Caches Cache: A Smaller, Faster Storage Device That Acts As A Staging Area For A Subset Of The Data In A Larger, Slower Device. Fundamental Idea Of A Memory Hierarchy: – For Each K, The Faster, Smaller Device At Level K Serv Apr 2th, 2024

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