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Random Features (Huang Et Al., 2014; May Et Al., 2017). The Approach Fell Out Of Favor As The State Of Mar 1th, 2024

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Literature For Low-voltage CMOS APS Pixels [1-9]. Recently, A Mixed-signal Design Technique, Called Supply Boosting (SBT), Was Demonstrated For Sub-1V Analog To Digital Converters Achieving Low-voltage And Low-power Operations In High-Vt CMOS Processes [10,11]. It Was Foreseen That The SBT Is Well Suited For Mar 2th, 2024

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