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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [May 2th, 2024

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Technology, Radio Frequency Identification (RFID) Has Drawn More And More Attention In Various Fields. In Recent Years, More And More Research Is Devoted To The Development And Introduction Of RFID. Enterprises Through The Proper Planning Of Introduction RFID To Reduce Costs And Even Improve Investment Efficiency. Mar 1th, 2024

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