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...Figure 8.39. Block Diagram For The Serial Adder. Sum A B = + Shift Register Shift Register Adder FSM Shift Register B A A B S Clock Example Of The Serial Adder A = A N-1 A N-2 ... A 0 B = B N-1 B N-2 ... B 0 A = S N-1 S N-2 ... S 0 = A + B Apr 10th, 2024.

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