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Standard Specific Gravity: 1.25 ± 1.29 At 20 C (68 F) 4.  
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Are Aligned, That Is, Each Has An Address That Is A

Multiple Of 4. MIPS Can Be Either Big-endian (that Is,

The Address Of Each Word Is The Address Of The “left-

most” Byte Of The Word) Or Little-endia May 1th,

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