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The System Is Built On The Altera®R Stratix®R II EP2S60 DSP Development Kit, As Shown In Figure 1. The On-board 12-bit Analog-to-digital Converter (ADC) AD9433 Is Used For The Data Sampling; While The 10/100 Single Chip PHY/MAC SMSC LAN91C111 Is Used For The UDP Communication Between May 2th, 2024

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