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Chapter 8 Memory Hierarchy And Cache Memory • Suppose Processor Has 2 Levels

Of Hierarchy: Cache And Main Memory • T Cache = 1 Cycle, T MM = 100 Cycles •
What Is The AMAT Of The Program From Example 1? $AMAT = T_{Cache} + MR_{Cache} (t_{MM}) = [1 + 0.375(100)] \text{ Cycles} = 38.5 \text{ Cycles}$ Memory Performance Example 2
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Amount Of Information Is Stored In Our Memory. It Is Sometimes Difficult To
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State Storage Prevails For The Majority (more Than 50%) Of The Iometertests
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The Memory/Storage Hierarchy And Virtual Memory Storage Device Speed Vs. Size
Facts: • CPU Needs Sub-nanosecond Access To Data To Run Instructions At Full
Speed • Fast storage (sub-nanosecond) Is Small (100-1000 Bytes) • Big Storage
(gigabytes) Is Slow (15 Nanoseconds) • Huge storage (terabytes) Is Glacially slow
(milliseconds) Goal: • Need Many Gigabytes Of Memory Mar 7th, 2024

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