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[5] Moon Gyung Kim, Byung In Moon, Sang Jun An, Dong Ryul Ryu, And Yong Surk Lee, "Implementation Of A Cycle-based Simulator For The Design Of A Processor Core", IEEE Asia-Pacific ASIC Conference (AP-ASIC), 1999. [6] Namseung Kim, Hoon Choi, Seungjong Lee, Seungwang Lee, In-Cheol Park And Chong-Min Kyung, Mar 3th, 2024

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