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Behavior Of A 6-bit D/A Converter. Evaluate Linear And Nonlinear Errors, Nonmonotonicy And Glitches And O Jan 1th, 2024Jitter Effects On Analog To Digital And Digital To Analog ...For Digital To Analog Conversion The Sample Clock Is Usually Derived From An AES Or S/ PDIf Bit Stream. And Like The Analog To Digital Converter, This Regeneration Process Can Introduce Jitter Into The Sample Clock May 2th, 2024.

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