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5th 1.29 6th 1.00 7th 0.84 8th 0.67 Reverse 3.30 Final Drive ... May 11th, 2024Embedded Memory (RAM: 1-PORT, RAM: 2-PORT, ROM: 1 ...RAM And ROM Parameter Settings. Table 2. Parameters For Altera_syncram. Use The Parameter List When Editing The Design File Manually. Name Legal Values Description. Operation_mode. SINGLE_PORT DUAL_PORT BIDIR_DUAL_PORT QUAD_PORT ROM Operation Feb 29th, 2024.

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