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Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A
Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic
Inversion Condition Mainly Used With Binary Logic Elements Where A Higher
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Import, 9 February 2017 By Shujen Chen (Author) 4.3 Out Of 5 Stars 22 Ratings. See Jan 15th, 2024.

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