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Www.vishay.com Vishay Revision: 23-Aug-12 1 Document Number: 45177 For Technical Questions, Contact: CmlI@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. Feb 21th, 2024

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MICROCIRCUIT, DIGITAL, ADVANCED CMOS, QUAD TWO ... MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification For. DEPARTMENT OF DEFENSE STANDARDS MIL-STD-883 - Test Method Standard Microcircuits. MIL-STD-1835 - Interface Standard Electronic Component Case Outlines. DEPARTMENT OF DEFENSE HANDBOOKS MIL-HDBK-103 - List Of Standard Microcircuit Drawings. Feb 22th, 2024 CD4066B CMOS Quad Bilateral Switch Datasheet (Rev. H) - ... • Analog-to-Digital And Digital-to-Analog Conversions • Digital Control Of Frequency, Impedance, Phase, And Analog-Signal Gain • Building Automation 3 Description The CD4066B Device Is A Quad Bilateral Switch Intended For The Transmission Or Multiplexing Of Analog Or Digital Signals. It Is Pin-for-pin Compatible Apr 25th, 2024 74LVC125A Low-Voltage CMOS Quad Buffer 74LVC125A Wwww.onsemi.com 3 MAXIMUM RATINGS Symbol Parameter Value Condition Unit VCC DC Supply Voltage -0.5 To +6.5 V VI DC Input Voltage  $-0.5 \leq VI \leq +6.5$  V VO DC Output Voltage  $-0.5 \leq VO \leq +6.5$  Output In 3-State V  $-0.5 \leq VO \leq VCC + 0.5$  Output In HIGH Or LOW State (Note 1) V IIK DC Input Diode Feb 27th, 2024.

A Quad CMOS Single-supply Op Amp With Rail-to-rail Output ... Centerline For Isothermal Reasons. The Rejection Of Thermal Waves From The Other Cells On The Chip Is Left To The High Degree Of Cross Coupling. From A Circuit Point Of View The Input Stage Is Also Well Apr 9th, 2024

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