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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [2th, 2024

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1) HID Proximity Reader 5355 2) CoreStation 3) HID Prox Card 125kHz 1. When You Take A Look At The Manual For CoreStation(CS-40), You Can See A Wiring Diagram For The Product. CoreStation Manual 2. In This Example, We Will Make Use Of “wiegand 2” Input As Well As Power Supply On This Side 2th, 2024

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1) Lectora De Proximidad HID 5355 2) CoreStation 3) Tarjeta HID Prox 125kHz 1. Cuando Dé Un Vistazo Al Manual De CoreStation (CS-40), Verá Un Diagrama De Cableado Para El Producto. Manual De CoreStation 2. En Este Ejemplo, Usaremos La Entrada Wiegand 2 Y La Fuente De Alimentación En Este Lado. 2th, 2024

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PIB300-2D+RLBD Panel Interface Module And Relay Board AD-400/401 Wireless Devices AD-300/301 Hardwired Devices Wiegand Or Clock And Data Integration For AD-300 And AD-301 Hardwired And AD-400 And AD-401 Wireless Devices Ether 3th, 2024

RS-485 Or Wiegand Or Clock And Data Integration For AD-300 ...
AD-400 Wireless Range To 00 In Tical Ilding Constrction To 000 Line Of Sight Www.allegi 1th, 2024

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